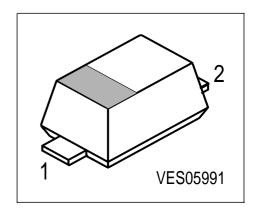
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Silicon Tuning Diode

Preliminary data

- For VHF 2-Band-hyperband-TV-tuners
- Very high capacitance ratio
- Low series inductance
- Low series resistance
- Extremely small plastic SMD package
- Excellent uniformity and matching due to "in-line" matching assembly procedure



Туре	Marking	Ordering Code		Pin Configuration		Package	
BB 689	E	Q62702-B0886	unmached	1 = C	2 = A	SCD-80	
BB 689	E	Q62702-B0890	in-line matched				

Maximum Ratings

Parameter	Symbol	Value	Unit
Diode reverse voltage	V_{R}	30	V
Peak reverse voltage ($R \ge 5k\Omega$)	V_{RM}	35	
Forward current	I _F	20	mA
Operating temperature range	T_{op}	- 55+150	°C
Storage temperature	$T_{ m stg}$	- 55+150	

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Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	<u> </u>
DC characteristics	•		•	•	•
Reverse current	I _R	-	-	10	nA
$V_{R} = 30 \text{ V}$					
Reverse current	I _R	-	-	200	μΑ
$V_{R} = 30 \text{ V}, \ T_{A} = 85 \text{ °C}$					
AC characteristics					
Diode capacitance	C_{T}				pF
$V_{R} = 1 V, f = 1 MHz$		51	56.5	61.5	
$V_{R} = 2 \text{ V}, f = 1 \text{ MHz}$		39.6	43.4	47.2	
$V_{R} = 25 \text{ V}, f = 1 \text{ MHz}$		2.6	2.8	3	
$V_{R} = 28 \text{ V}, f = 1 \text{ MHz}$		2.5	2.7	2.9	
Capacitance ratio	C_{T2}/C_{T25}	14.5	15.5	17	-
$V_{R} = 2 \text{ V}, \ V_{R} = 25 \text{ V}, \ f = 1 \text{ MHz}$					
Capacitance ratio	$C_{\text{T1}}/C_{\text{T28}}$	18	20.9	23.2	1
$V_{R} = 1 \text{ V}, V_{R} = 28 \text{ V}, f = 1 \text{ MHz}$					
Capacitance ratio 1)	$\Delta C_{T}/C_{T}$	-	-	2	%
$V_{R} = 1 \text{ V}, V_{R} = 28 \text{ V}, f = 1 \text{ MHz}$					
Series resistance	r _s	-	0.85	-	Ω
$V_{R} = 8 \text{ V}, f = 470 \text{ MHz}$					
Series inductance chip to ground	L _s	-	0.6	-	nH

¹⁾ In-line matching. For details please refer to Application Note 047

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Diode capacitance $C_T = f(V_R)$

f = 1MHz

